

JC511 U.S. PTO
09/514762
02/28/00

Subclass	Class
02	28
ISSUE CLASSIFICATION	

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PATENT NUMBER

U.S. UTILITY Patent Application

SCANNED	O.I.P.E. RG Q.A. CHW	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/514762	D	251 29	267 853	284 3729	Patel Chonag

APPLICANTS
Donald Abbott
John Cotugno
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TITLE
Double sided flexible circuit for integrated circuit packages and method of manufacture

PTO-2040
12/99

CPA

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			ISSUE BATCH NUMBER	

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